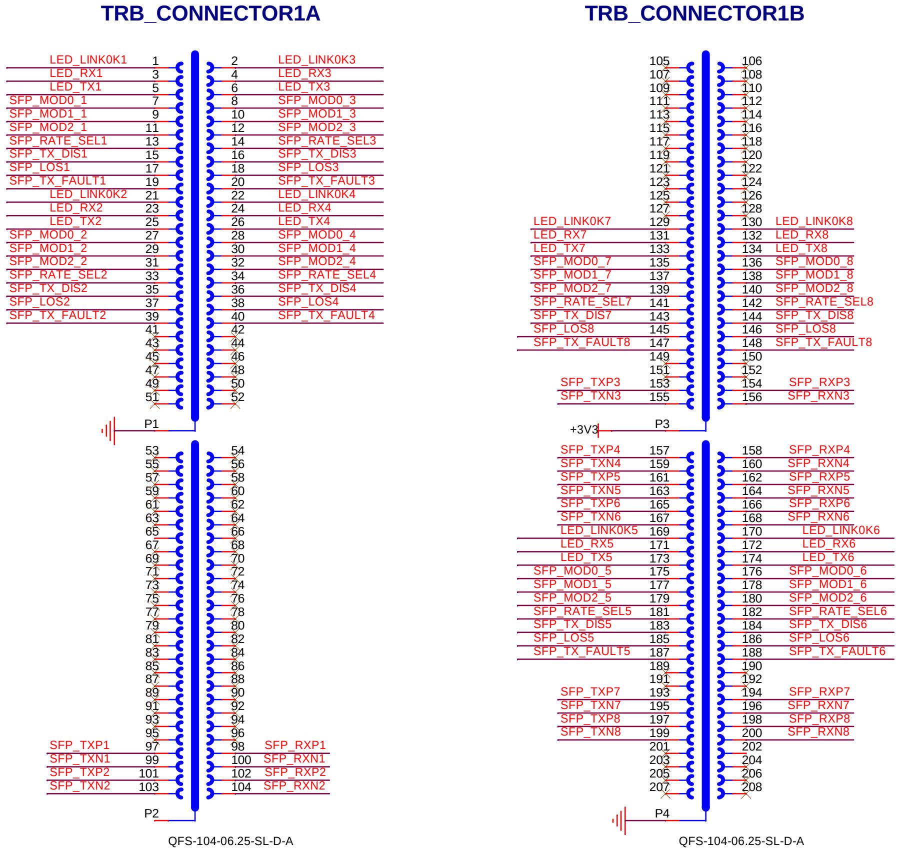
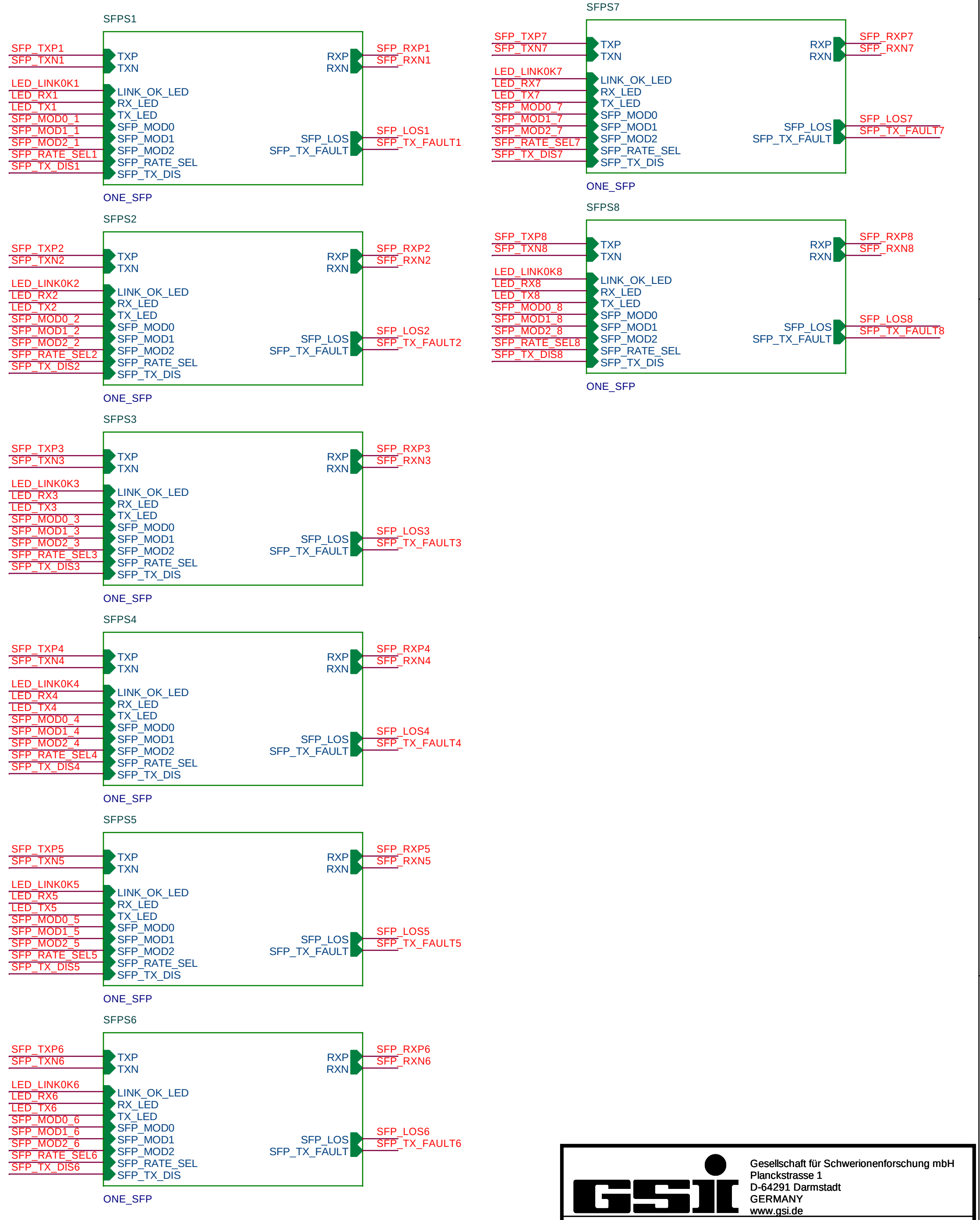
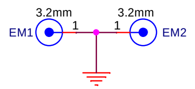





```
Mit der Fertigung haben wir uns darauf geeinigt, daß
die einzelnen Cages belly-to-belly auf einem PCB
mit ca. 3.1mm Dicke liegen, so daß die Beinchen
nicht aneinander stoßen.
6 auf der Vorderseite und 2 auf der Rückseite.
```

```
Zeichnung mit den Dimensionen der Platine:

K:\GsiJob\HADES\TRBv3\SFP-Addon2\SFPAddOn_Zeichnung.pdf
```



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D-64291 Darmstadt
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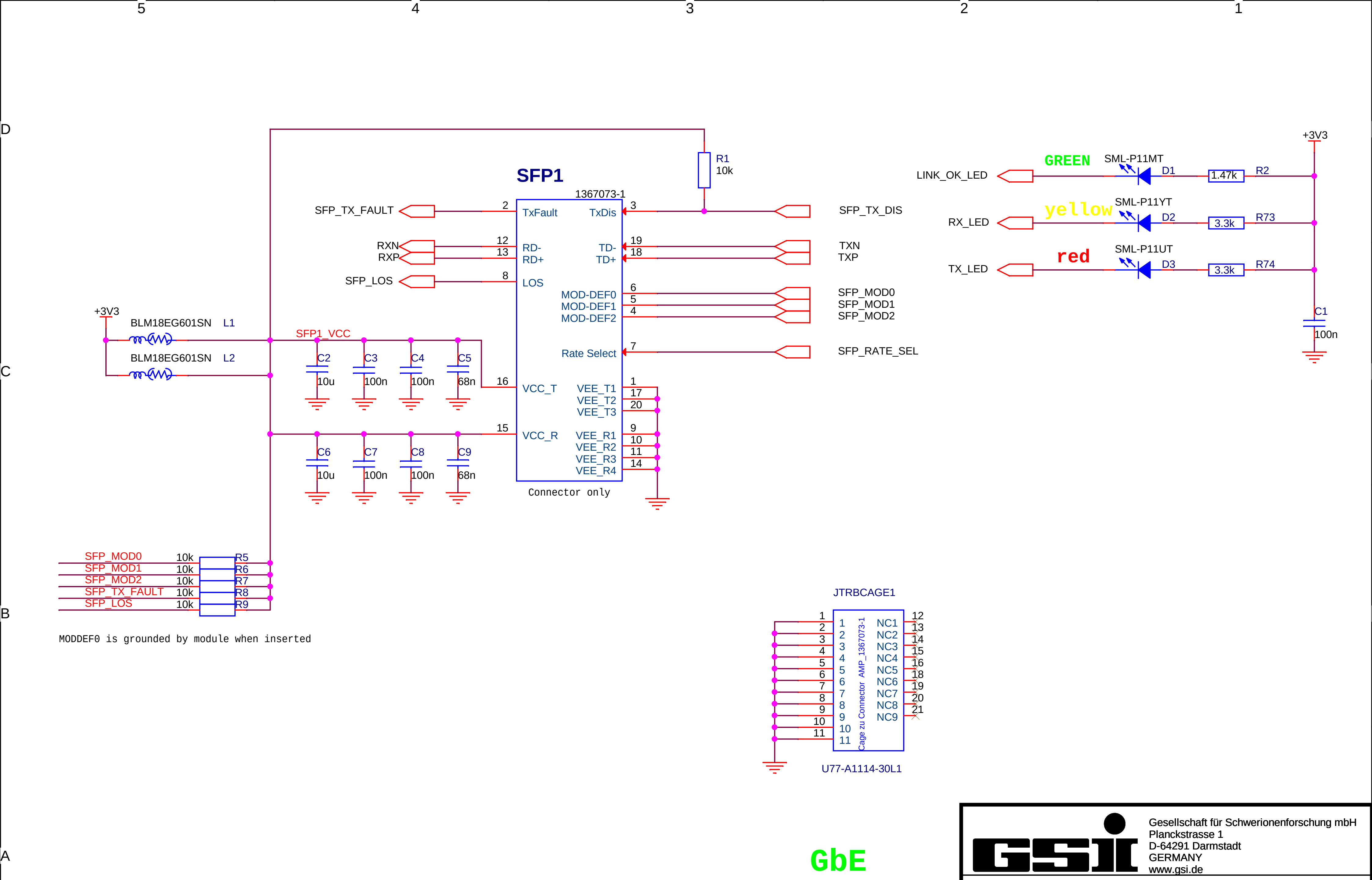
01_TRB_CONNECTOR

Design: K:\GSIJOB\HADES\TRBV3\SFP-ADDON2\SFP_ADDON2.DSN	
Modified: Thursday, July 06, 2017	Size: A3 Page: 1 / 9
Designer: M.Traxler	Layouter: H.Kayan



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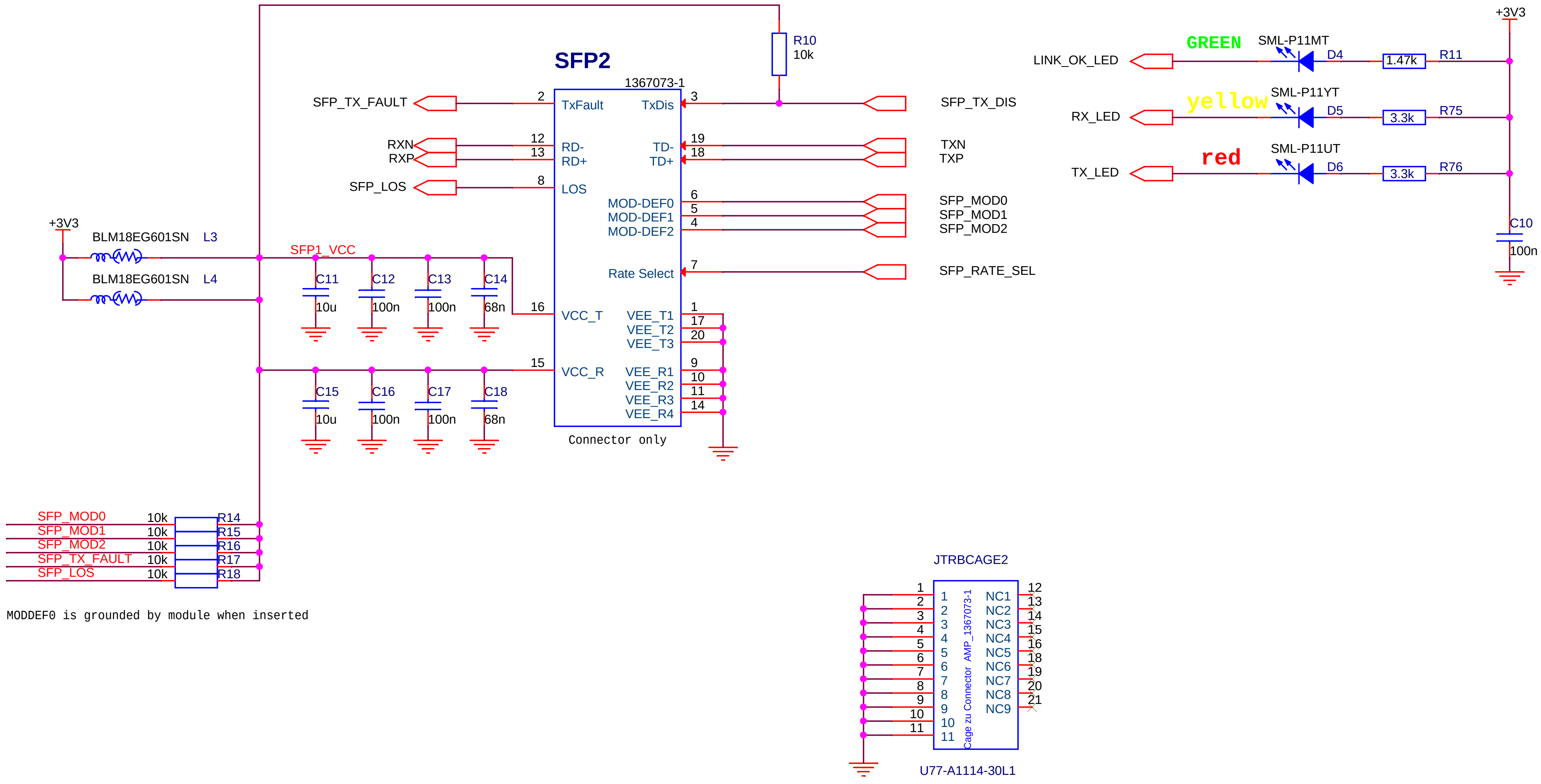
SFP

Design: K:\GSIJOB\HADES\TRBV3\SFP-ADDON2\SFP_ADDON2.DSN

Modified: Thursday, July 06, 2017 | Size: A4 | Page: 2 / 9

Designer: M.Traxler | Layouter: H.Kavan



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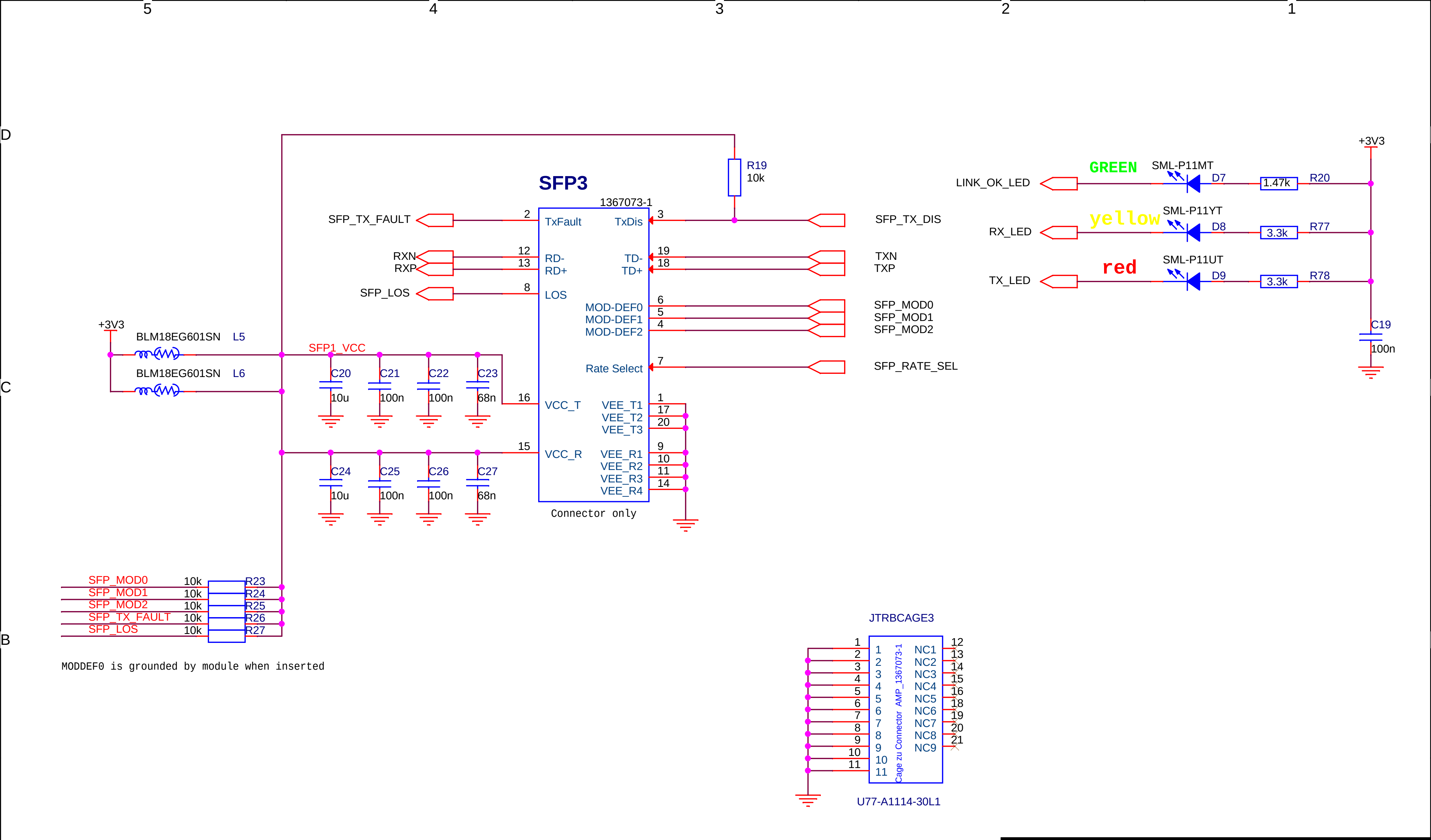
SFP

Design: K:\GSIJOB\HADES\TRBV3\SFP-ADDON2\SFP_ADDON2.DSN
Modified: Thursday, July 06, 2017 | Size: A4 | Page: 3 / 9
Designer: M.Traxler | Layouter: H.Kavan



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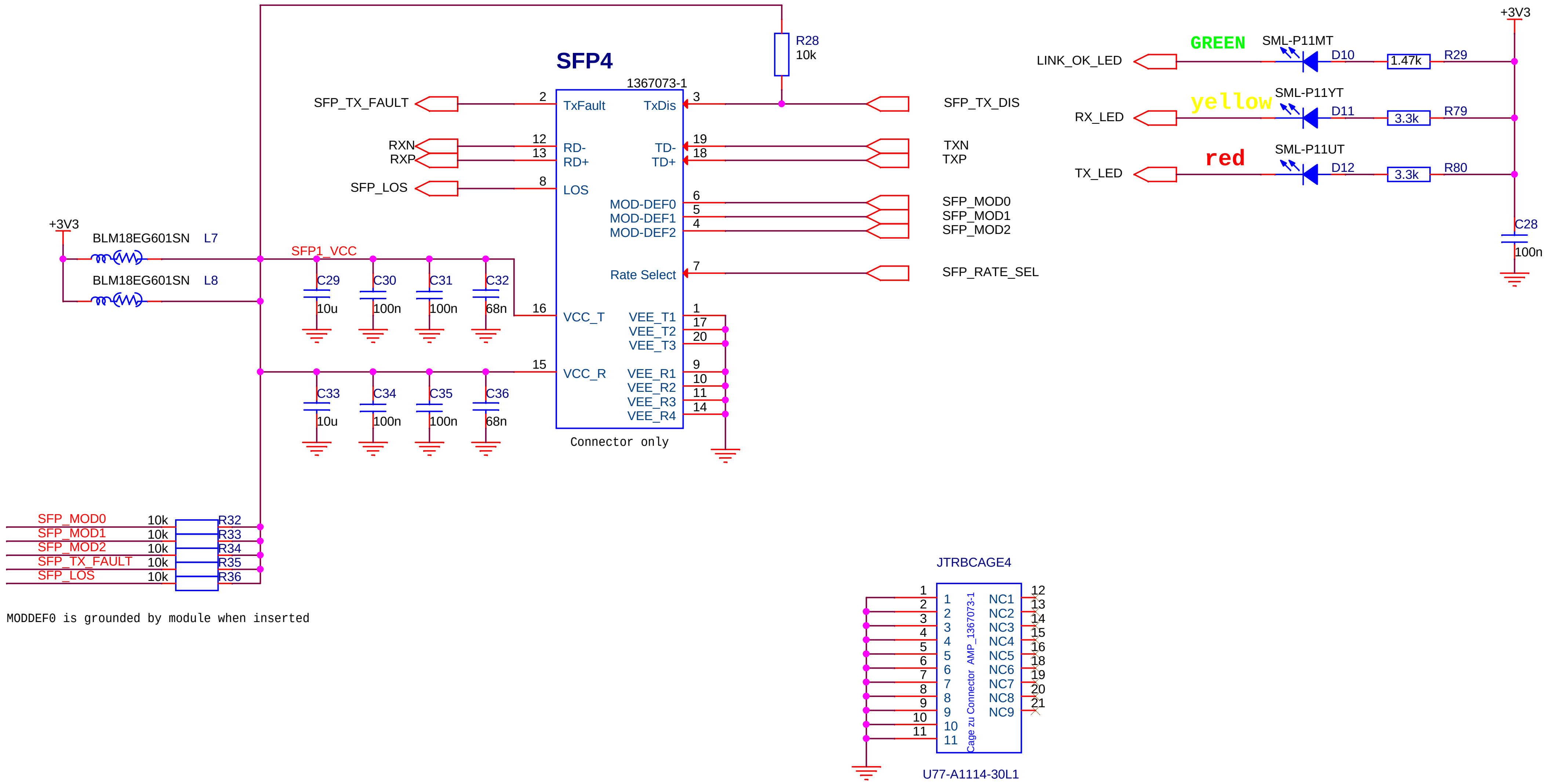
SFP

Design: K:\GSIJOB\HADES\TRBV3\SFP-ADDON2\SFP_ADDON2.DSN
Modified: Thursday, July 06, 2017 | Size: A4 | Page: 4 / 9
Designer: M.Traxler | Layouter: H.Kavan



GbE

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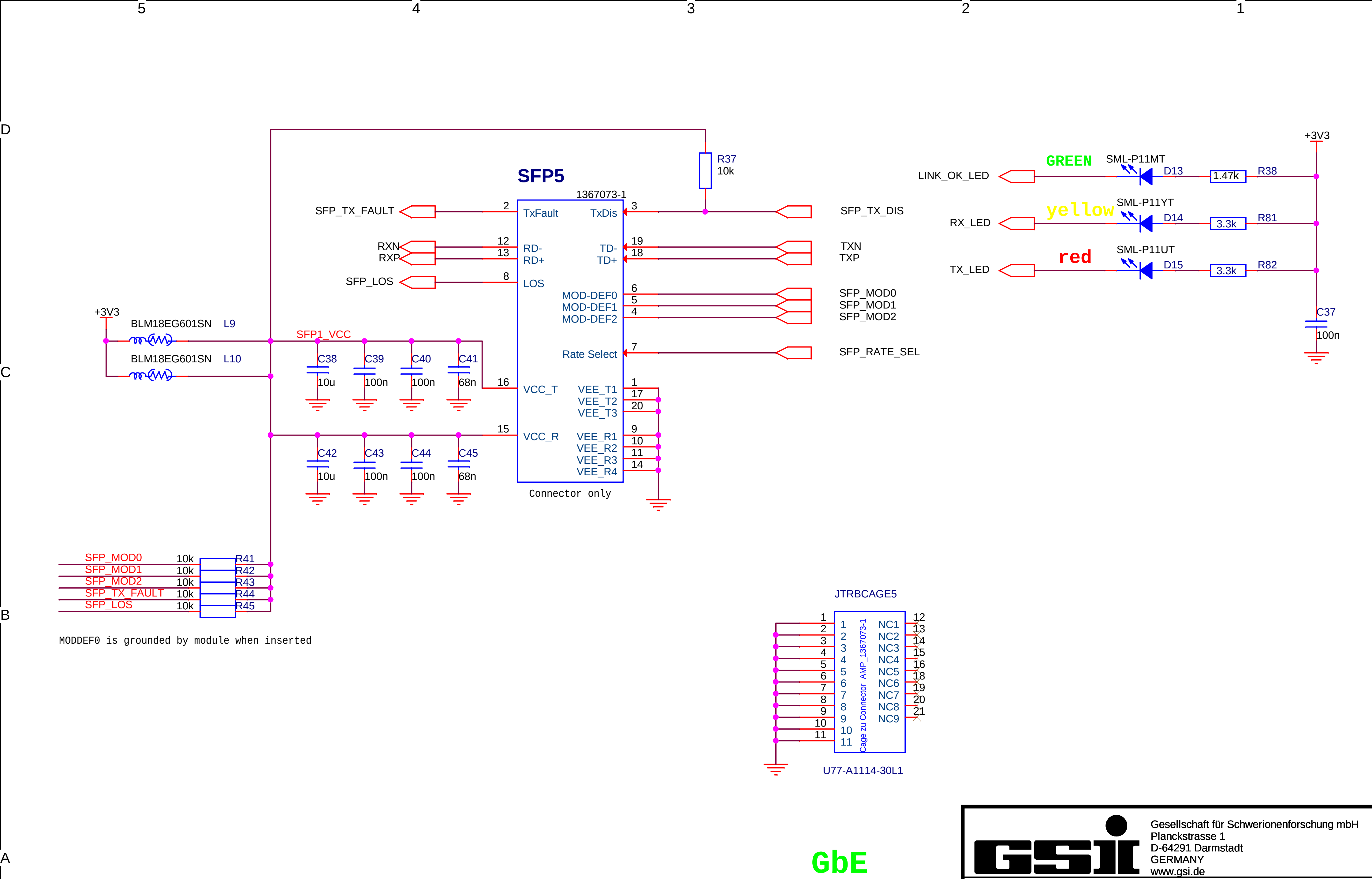
SFP

Design: K:\GSIJOB\HADES\TRBV3\SFP-ADDON2\SFP_ADDON2.DSN		
Modified: Thursday, July 06, 2017	Size: A4	Page: 5 / 9
Designer: M.Traxler	Layouter: H.Kavan	



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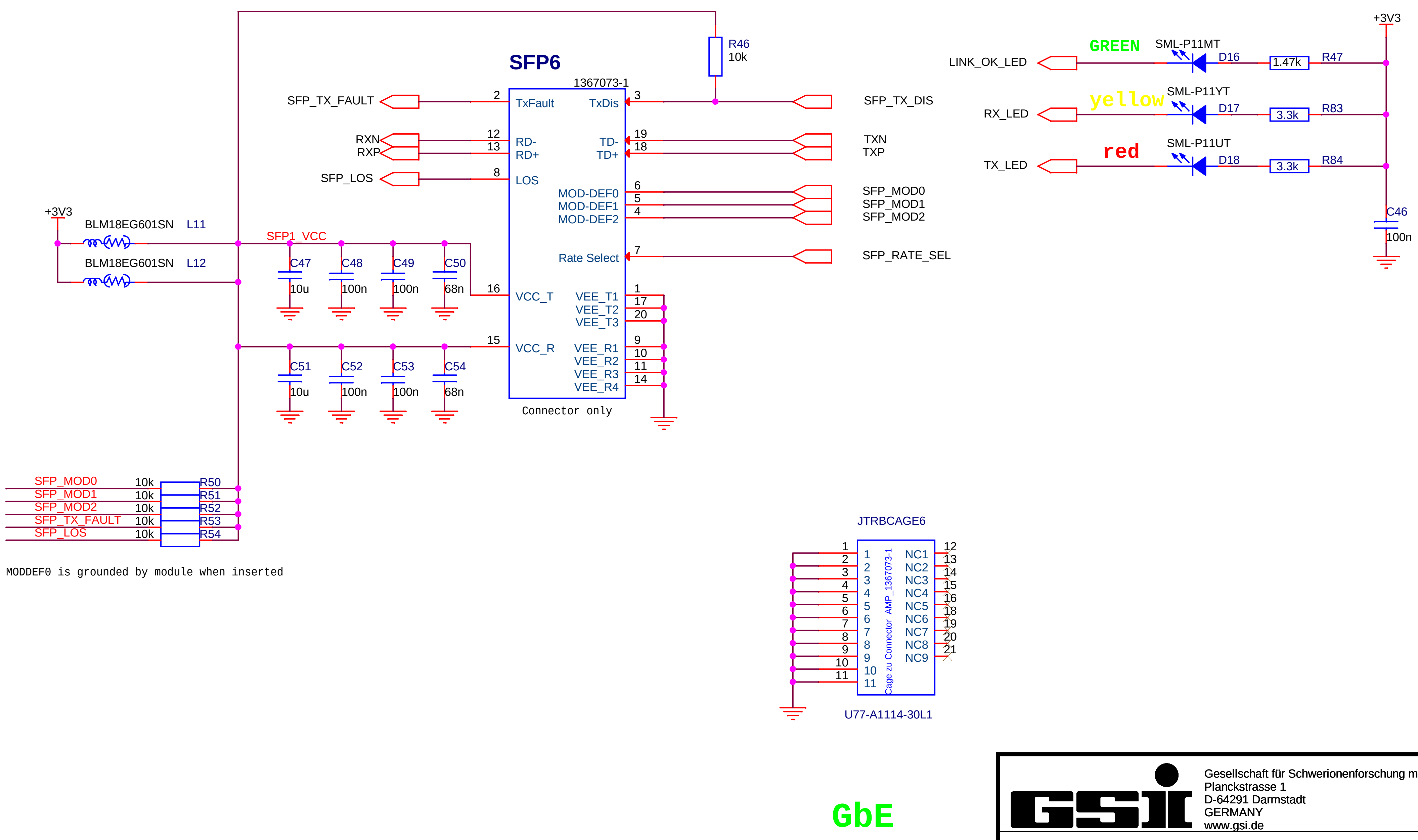
SFP

Design: K:\GSIJOB\HADES\TRBV3\SFP-ADDON2\SFP_ADDON2.DSN
Modified: Thursday, July 06, 2017 | Size: A4 | Page: 6 / 9
Designer: M.Traxler | Layouter: H.Kavan



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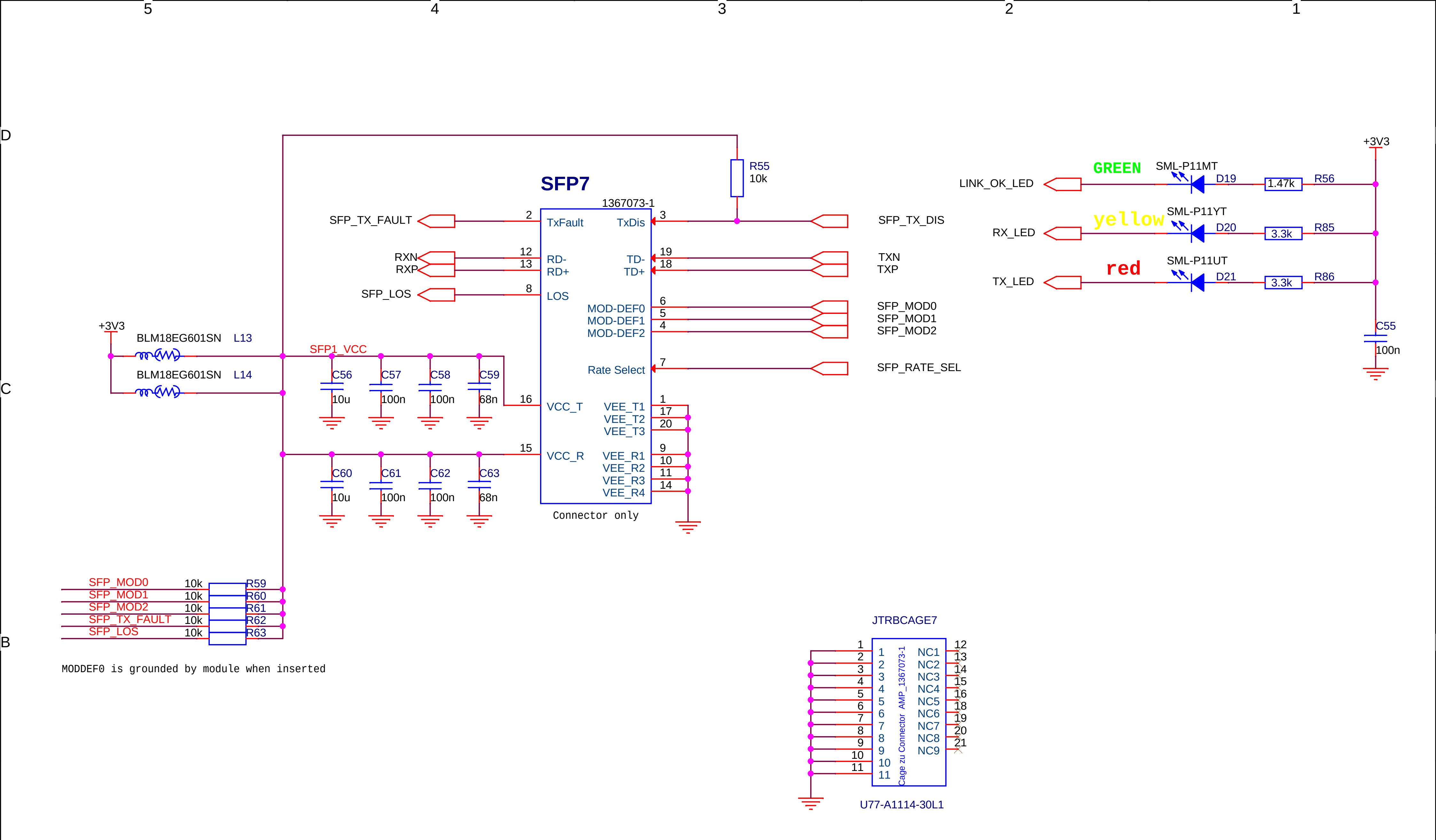
SFP

Design: K:\GSIJOB\HADES\TRBV3\SFP-ADDON2\SFP_ADDON2.DSN

Modified: Thursday, July 06, 2017 | Size: A4 | Page: 7 / 9

Designer: M.Traxler | Layouter: H.Kavan



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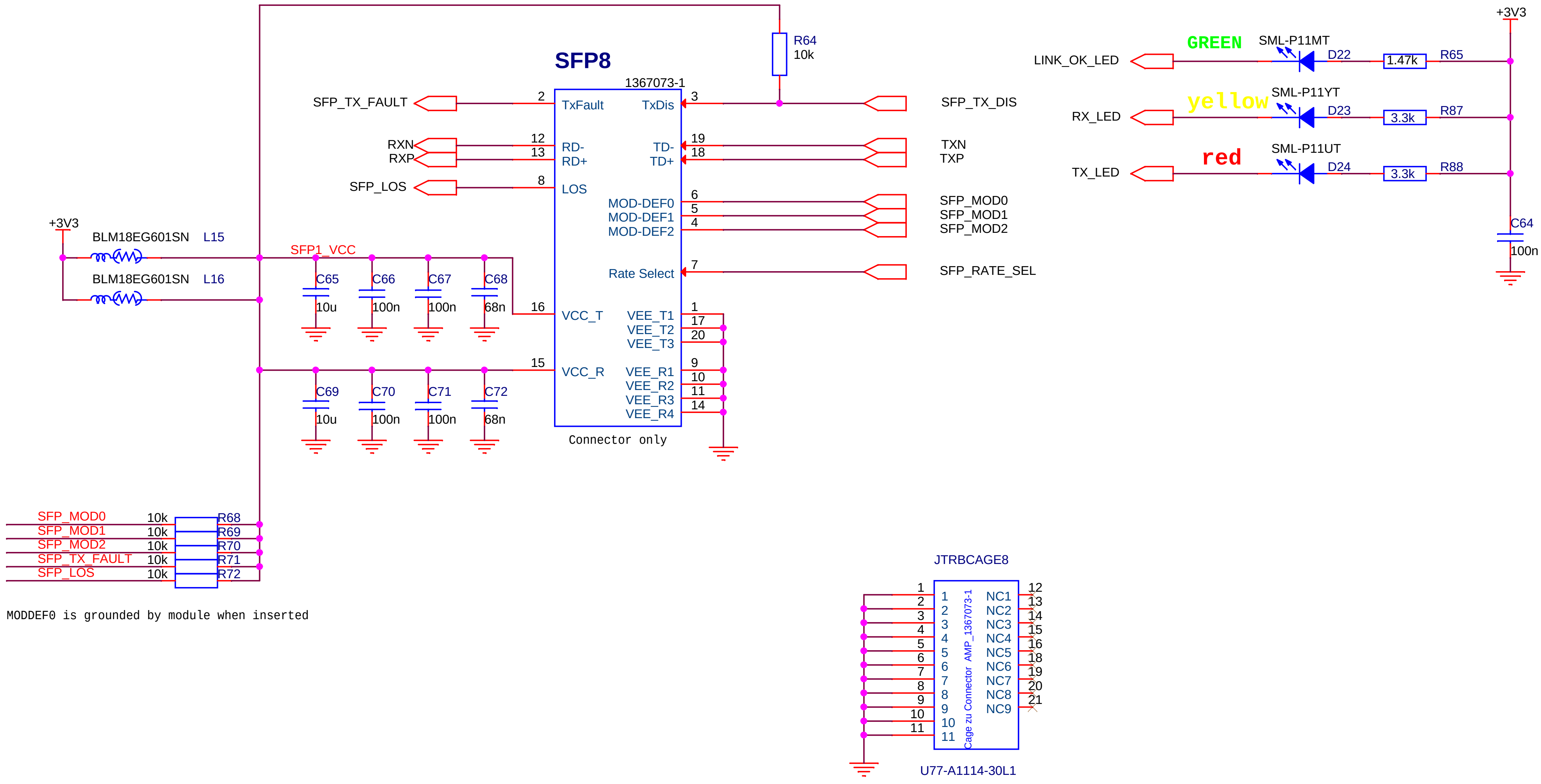
SFP

Design: K:\GSIJOB\HADES\TRBV3\SFP-ADDON2\SFP_ADDON2.DSN

Modified: Thursday, July 06, 2017 | Size: A4 | Page: 8 / 9

Designer: M.Traxler | Layouter: H.Kavan



GbE

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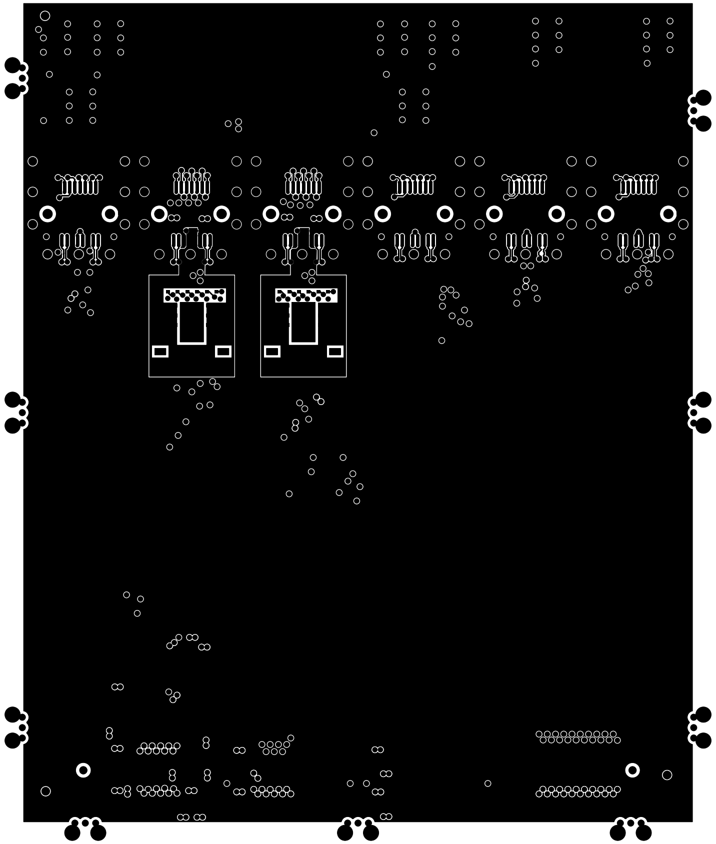
SFP

Design: K:\GSIJOB\HADES\TRBV3\SFP-ADDON2\SFP_ADDON2.DSN	
Modified: Thursday, July 06, 2017	Size: A4 Page: 9 / 9
Designer: M.Traxler	Layouter: H.Kavan

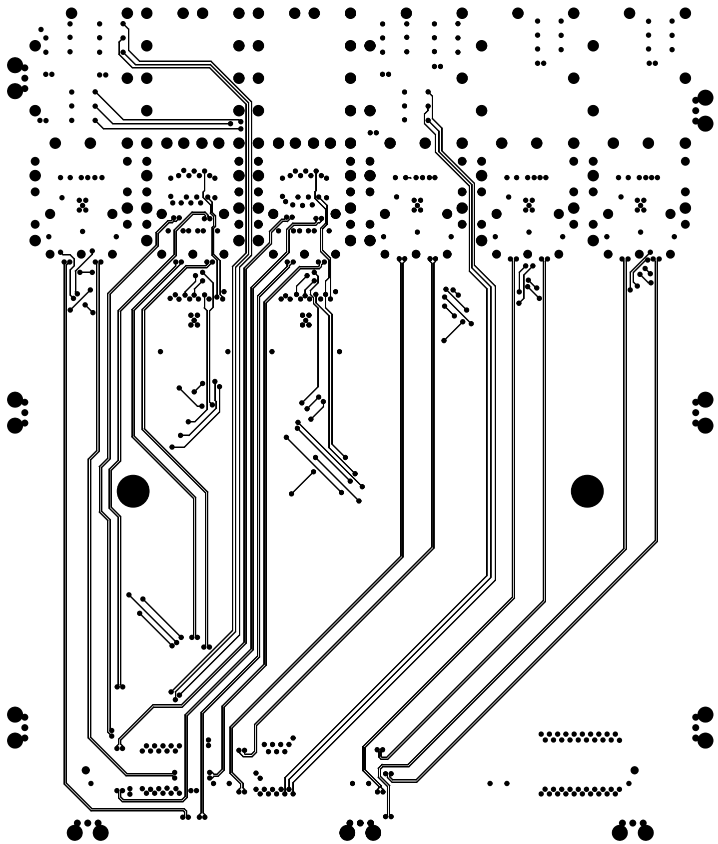


Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

TOP- GND-Plane

Layer Nickname

Top

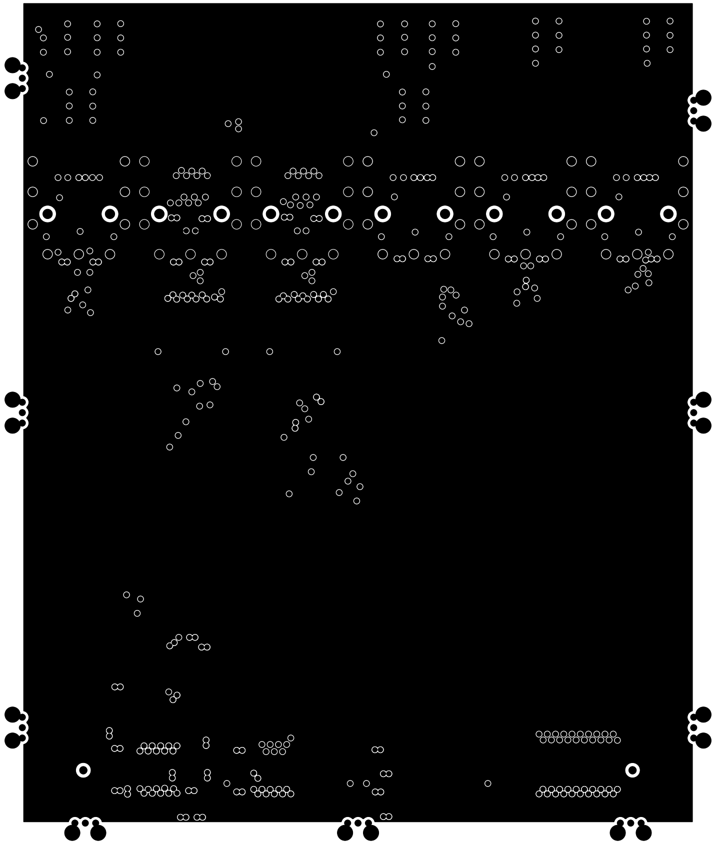


Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

I01

IN1- Diff.Pairs, Signals

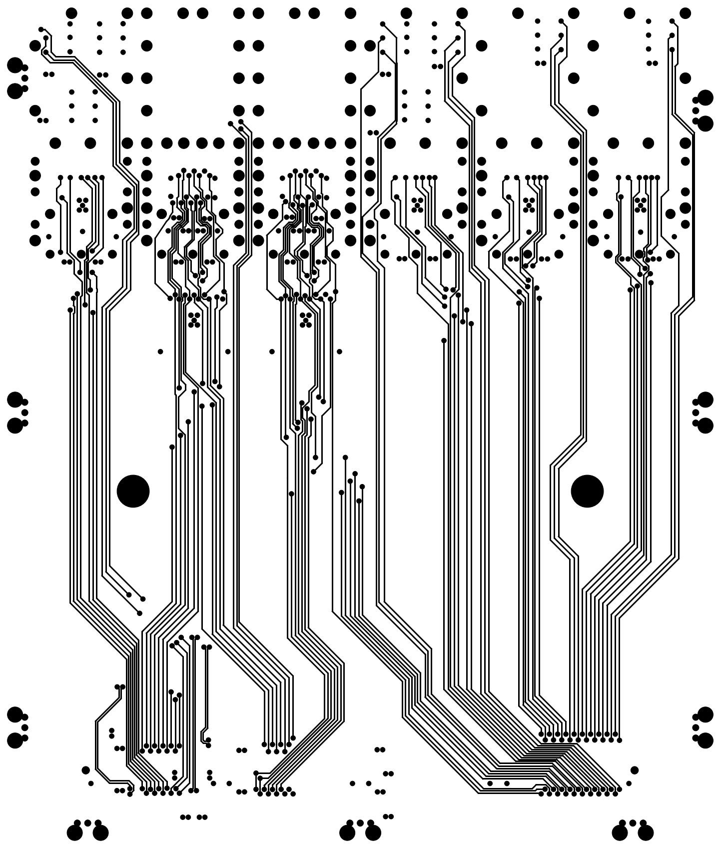


Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

IO2

IN2- Gnd-Plane

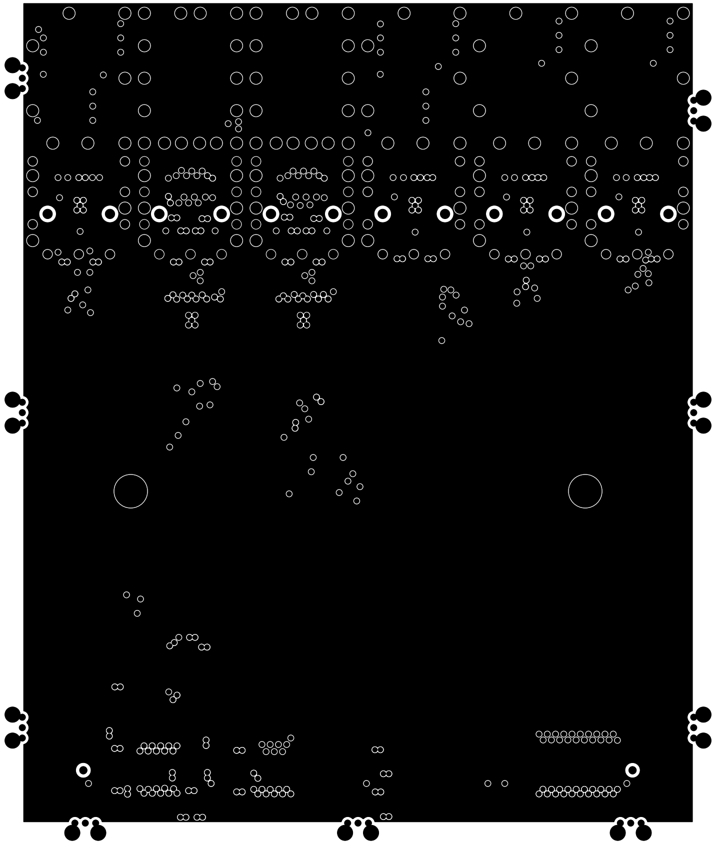


Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

I03

IN3- Diff. Pairs, Signals

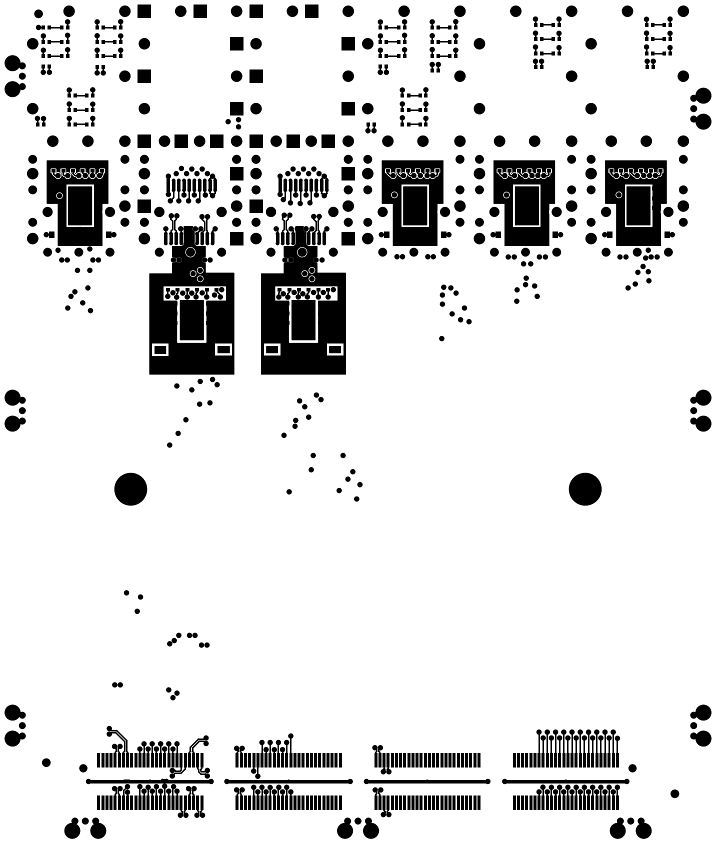


Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

I04

IN4- +3.3V-Plane

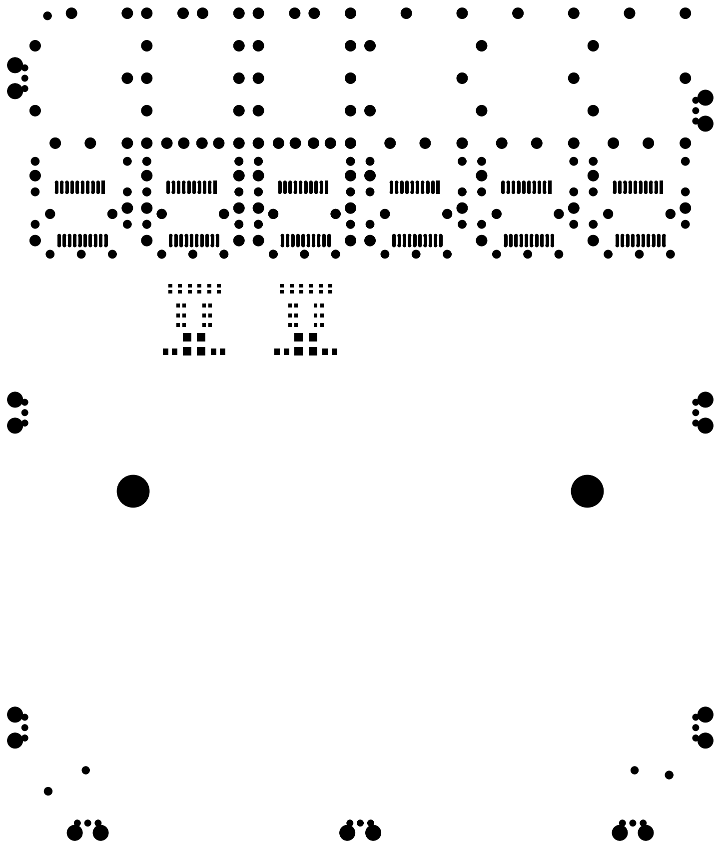


Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

Bot

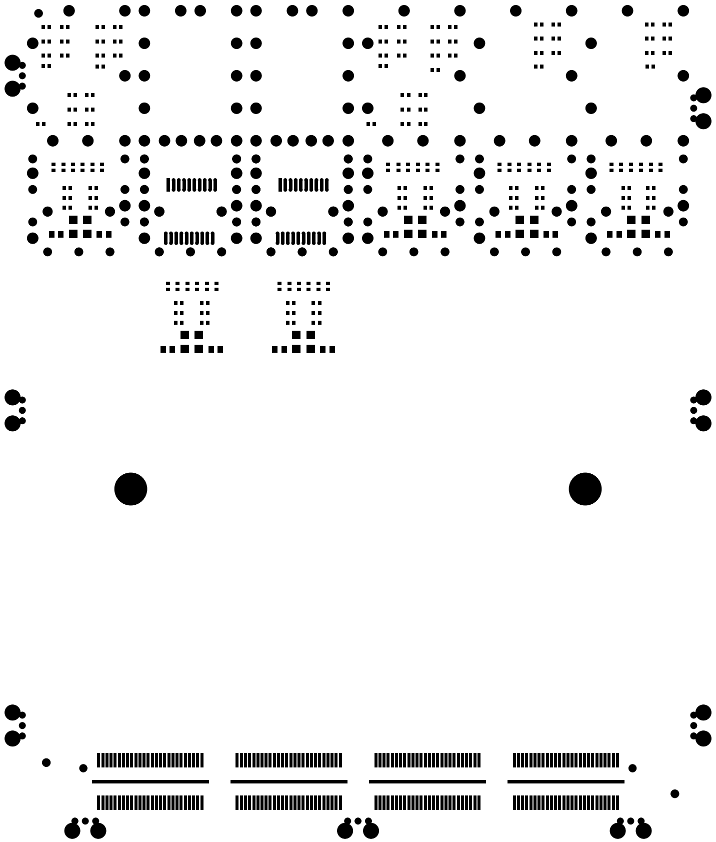
BOT- Signals



Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

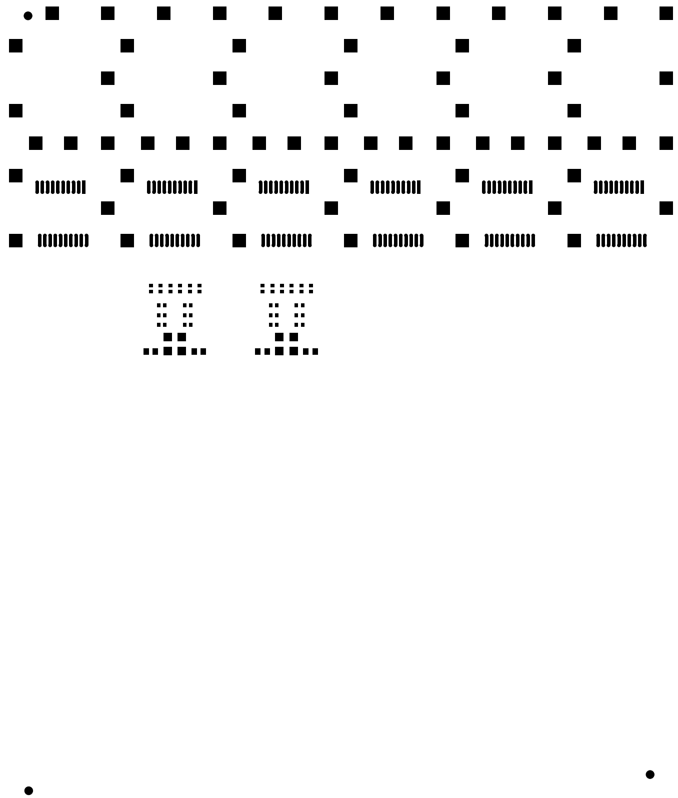
Smt



Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

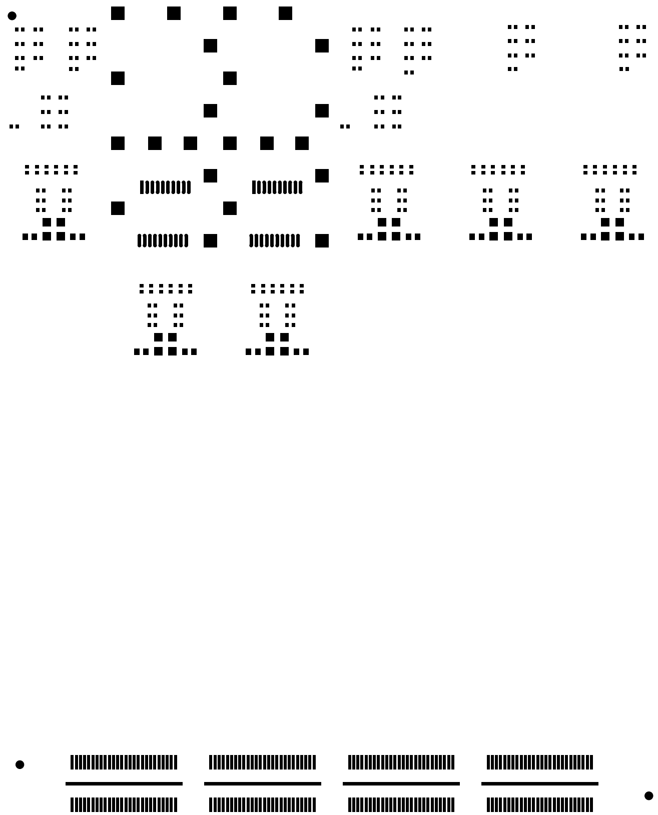
Smb



Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

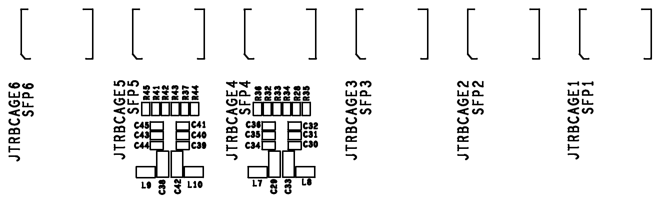
Spt



Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

Spb



EM1

EM2

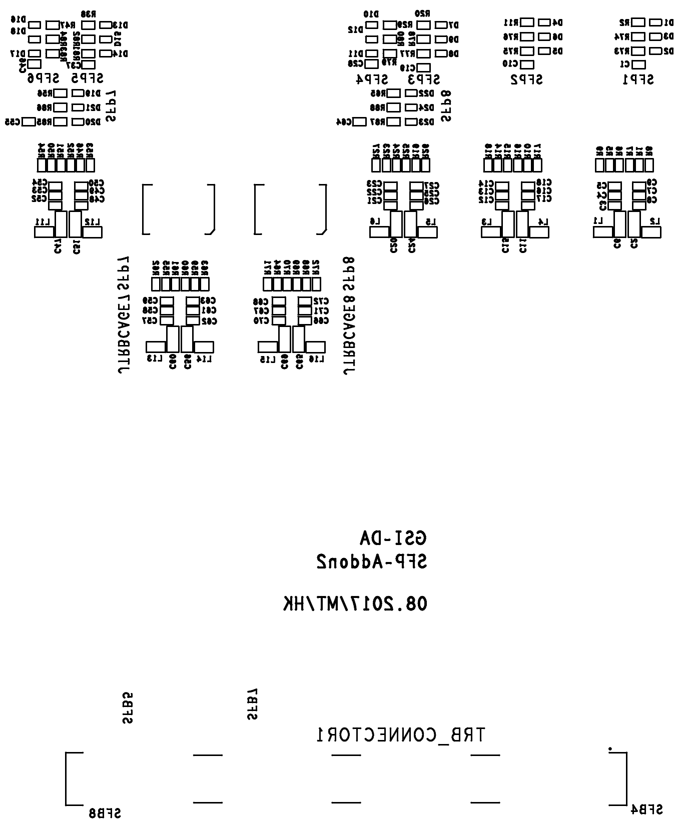
GSI-DA
SFP-Addon2

08.2017/MT/HK

Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

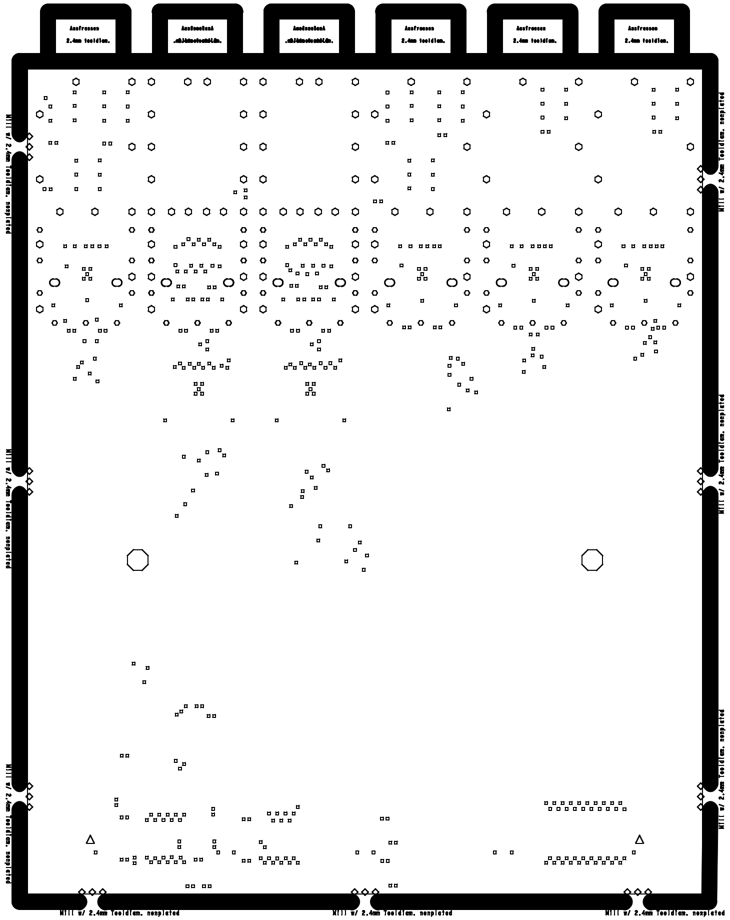
Sst



Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

Ssb



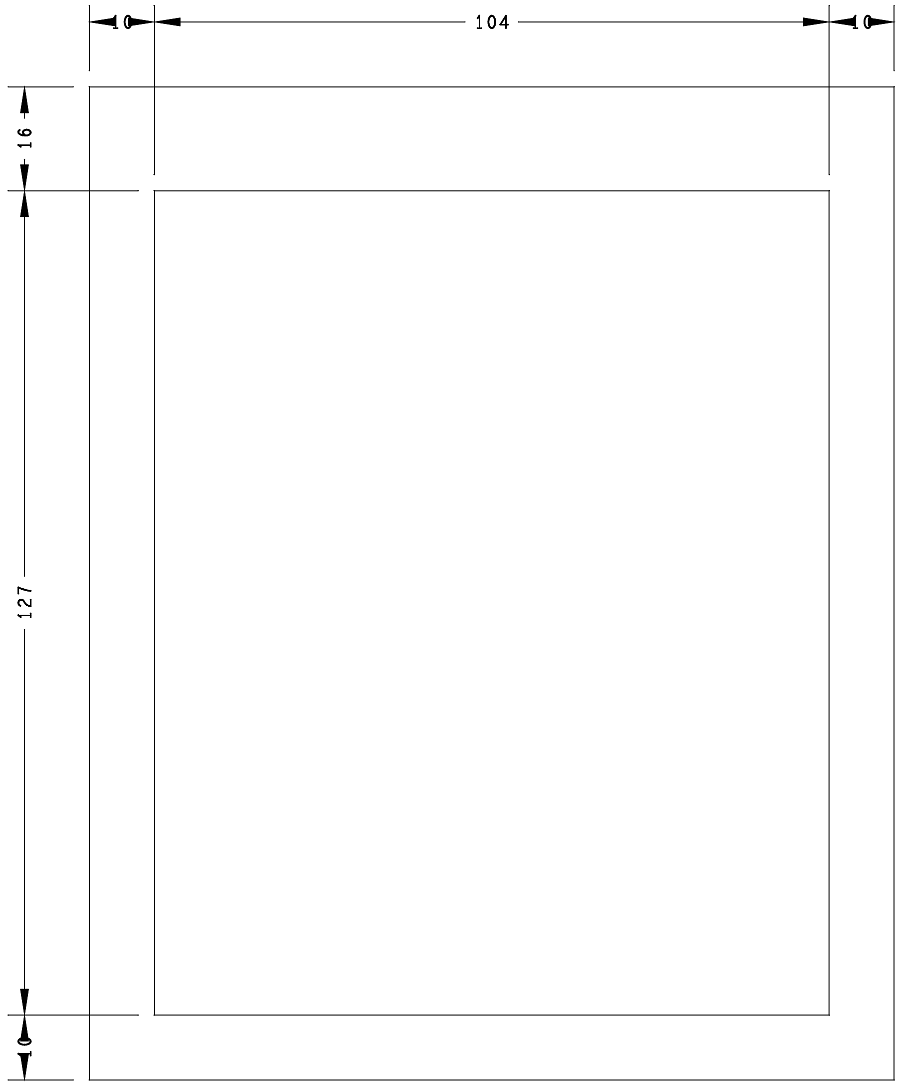
DRILL CHART: TOP to BOTTOM			
ALL UNITS ARE IN MILLIMETERS			
FIGURE	SIZE	PLATED	QTY
·	0.4	PLATED	1
▫	0.4	PLATED	458
○	0.8	PLATED	72
○	1.1	PLATED	88
⯃	3.2	PLATED	2
◇	1.0	NON-PLATED	27
△	1.2	NON-PLATED	2
⬭	1.5	NON-PLATED	16
⬯	2.4	NON-PLATED	18

TOTAL HOLES: 684

Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

Drd

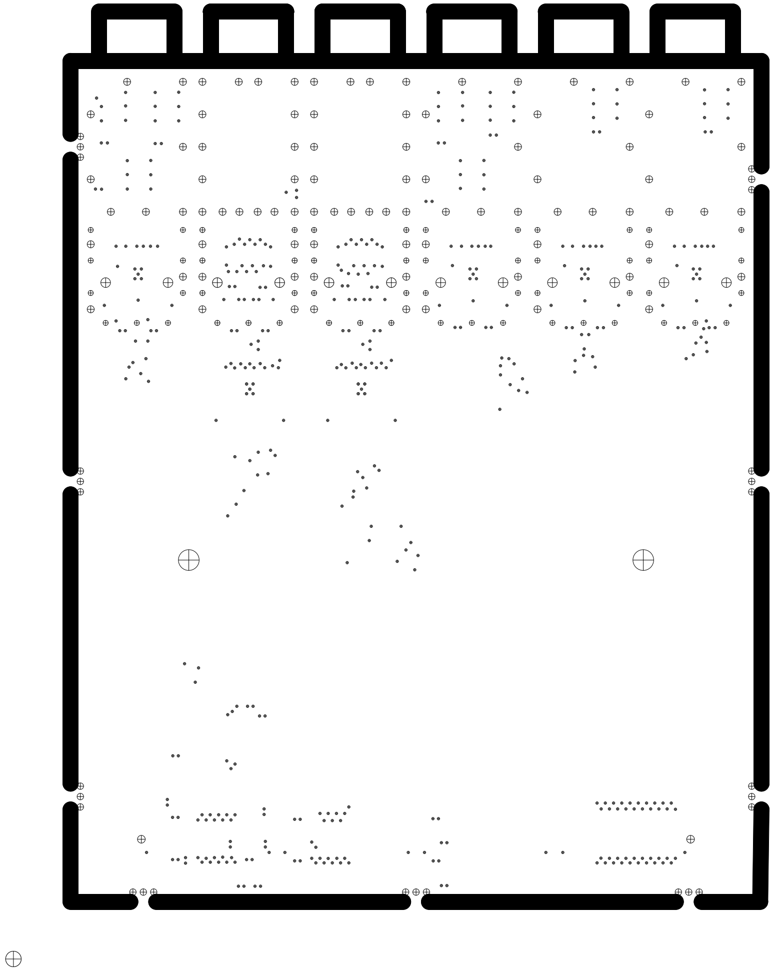


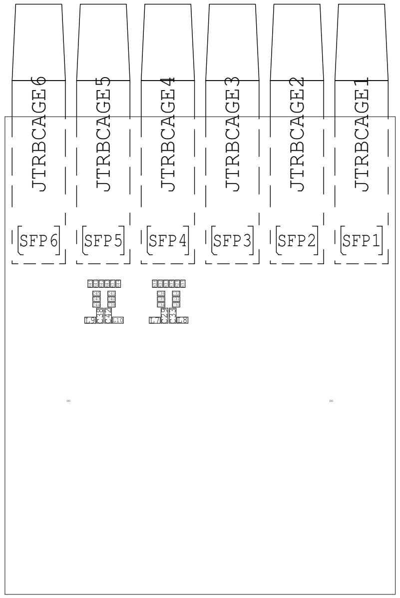
LP-Dicke = 3,8mm

Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

Fab

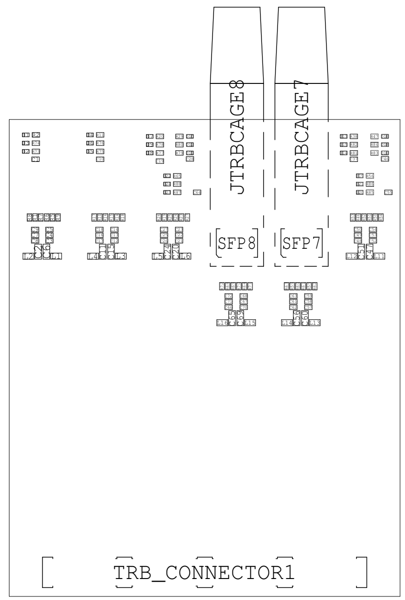




Jobname	Date	Designer	Layouter
SFP-Addon2	08.2017	M.Traxler	H.Kayan

Layer Nickname

Ast



Layouter	Designer	Date	Jobname
H.Kayan	M.Traxler	08.2017	SFP-Addon2

Layer Nickname

Asb

Capture CIS Standard Bill Of Materials - Compressed Report
Report Created on Thursday Jul 06 14:17:51 2017

Layouter = H.Kayan
Designer = M.Traxler

Item Number	Quantity	Part Referenc	DESCRIPTION	PART_CASE	Value	MANUFACTUI	MANUFACTUI	DATASHEET	COMMISSION	APPROVED	COMPONENT_ID
1	40	C1,C3,C4,C7,C	Capacitor, Cer	SMD-0402	100n				G.May	G.May	24583
2	16	C2,C6,C11,C1!	Capacitor, Cer	SMD-0805	10u	GRM21BR71A	Murata	K:\GsiLib\Data	M.Traxler	P.Skott	24740
3	16	C5,C9,C14,C1	Capacitor, Cer	SMD-0402	68n				G.May	G.May	21811
4	8	D1,D4,D7,D10	LED, Single, G	SMD-0402	SML-P11MT	SML-P11MT	Rohm	K:\GsiLib\Data	M.Traxler	P.Skott	25441
5	8	D2,D5,D8,D11	LED, Single, Ye	SMD-0402	SML-P11YT	SML-P11YT	Rohm	K:\GsiLib\Data	M.Traxler	P.Skott	25442
6	8	D3,D6,D9,D12	LED, Single, Re	SMD-0402	SML-P11UT	SML-P11UT	Rohm	K:\GsiLib\Data	M.Traxler	P.Skott	25444
7	2	EM1,EM2	Connector, Dr		3.2mm	Do not order	None		P.Wieczorek	G.May	24737
8	8	JTRBCAGE1-JT	Connector, Ca		U77-A1114-3(	U77-A1114-3(	Amphenol	K:\GsiLib\Data	M.Traxler	S.Voltz	25558
9	16	L1-L16	Inductor, RF-C	SMD-0603	BLM18EG601!	BLM18EG601!	Murata	K:\GsiLib\Data	M.Traxler	P.Skott	25264
10	48	R1,R5-R10,R1	Resistor, Thick	SMD-0402	10k	RC1005F103C	Samsung		G.May	G.May	21248
11	8	R2,R11,R20,R	Resistor, Thick	SMD-0402	1.47k				G.May	G.May	21169
12	16	R73-R88	Resistor, Thick	SMD-0402	3.3k				G.May	G.May	22831
13	8	SFP1-SFP8	Connector, So		1367073-1	1367073-1	Amphenol	K:\GsiLib\Data	M.Traxler	S.Voltz	20871
14	1	TRB_CONNEC	Connector, So		QFS-104-06.2!	QFS-104-06.2!	Samtec	K:\GsiLib\Data	M.Traxler	P.Skott	25423